



Certificate / Certificat Zertifikat / 合格証

MII 1211027 C001

exida hereby confirms that the:

SSX/SST Isolator/Splitter

**Moore Industries - International
North Hills, CA - USA**

The manufacturer
may use the mark:



Has been assessed per the relevant requirements of:

IEC 61508 : 2010 Parts 1-7

and meets requirements providing a level of integrity to:

Systematic Capability: SC 3 (SIL 3 Capable)

Random Capability: Type A, Route 1_H Device

**PFH/PFD_{avg} and Architecture Constraints
must be verified for each application**

Revision 3.0 February 28, 2023

Surveillance Audit Due
March 1, 2026

Safety Function:

The SSX/SST transmits the input signal to the output port(s) within the stated safety accuracy.

Application Restrictions:

The unit must be properly designed into a Safety Instrumented Function per the Safety Manual requirements.



Evaluating Assessor

Certifying Assessor

MII 1211027 C001

Systematic Capability: SC 3 (SIL 3 Capable)

Random Capability: Type A, Route 1_H Device

PFH/PFD_{avg} and Architecture Constraints must be verified for each application

Systematic Capability :

The product has met manufacturer design process requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer.

A Safety Instrumented Function (SIF) designed with this product must not be used at a SIL level higher than stated.

Random Capability:

The SIL limit imposed by the Architectural Constraints must be met for each element.

**SSX/SST
Isolator/Splitter**

IEC 61508 Failure Rates in FIT¹

Model Number	λ_{SD}	λ_{SU}	λ_{DD}	λ_{DU}
4-20 mA loop SSX/4-20mA/4-20MA/12-42DC [DIN]	0	157	0	53
4-20 mA loop SST/4-20mA/4-20MA/24DC [DIN]	0	244	0	65
4-20 mA loop SST/4-20mA/2X4-20MA/117AC [DIN]	0	293	0	77

Note: See Safety Manual/FMEDA Report for failure rates of other models

¹ FIT = 1 failure / 10⁹ hours

SIL Verification:

The Safety Integrity Level (SIL) of an entire Safety Instrumented Function (SIF) must be verified via a calculation of PFH/PFD_{avg} considering redundant architectures, proof test interval, proof test effectiveness, any automatic diagnostics, average repair time and the specific failure rates of all products included in the SIF. Each element must be checked to assure compliance with minimum hardware fault tolerance (HFT) requirements.

The following documents are a mandatory part of certification:

Assessment Report: MII 12-11-027 R002 V3 R1 (or later)

Safety Manual: 206-792-00H June 2018 (or later)



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